TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SZ00AFS

2-Input NAND Gate

Features

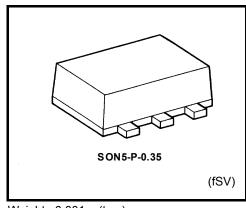
• High output current :±24 mA (min) at V_{CC} = 3 V

• Super high speed operation : t_{pd} = 2.4 ns (typ.)

at V_{CC} = 5 V, 50 pF

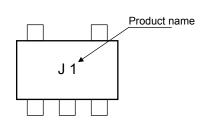
• Operating voltage range : V_{CC} = 1.65 to 5.5 V

• 5.5-V tolerant inputs

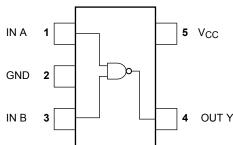


Weight: 0.001 g (typ.)

Marking



Pin Assignment (top view)



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	–0.5 to 6	V
DC input voltage	V _{IN}	–0.5 to 6	V
DC output voltage	V _{OUT}	–0.5 to V _{CC} +0.5V	٧
Input diode current	I _{IK}	-20	mA
Output diode current	lok	±20 (Note1)	mA
DC output current	lout	±50	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	PD	50	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note:

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

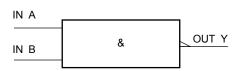
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: $V_{OUT} < GND, V_{OUT} > V_{CC}$

Start of commercial production 2008-02



IEC Logic Symbol



Truth Table

Α	В	Υ
L	L	Н
L	Н	Н
Н	L	Н
Н	Н	L

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	1.65 to 5.5	V
Supply voltage		1.5 to 5.5 (Note 2)	V
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	V _{OUT}	0 to V _{CC}	٧
Operating temperature	T _{opr}	-40 to 85	°C
	dt/dv	0 to 20 (V $_{CC}$ = 1.80 V \pm 0.15 V, 2.5 V \pm 0.2 V)	
Input rise and fall time		0 to 10 (V _{CC} = 3.3 V \pm 0.3 V)	ns/V
		0 to 5 (V $_{CC} =$ 5.0 V \pm 0.5 V)	

Note 2: Data retention only

Electrical Characteristics

DC Characteristics

Characteristics Symbol		bol Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit
				V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
High-level input VIH		_		1.65 to 1.95	V _{CC} × 0.75	_	_	V _{CC} × 0.75	_	- v
				2.3 to 5.5	V _{CC} × 0.7	_	_	V _{CC} × 0.7	_	
						_	V _{CC} × 0.25	_	V _{CC} × 0.25	.,
Low-level input voltage V _{II}	V _{IL}		_	2.3 to 5.5	_	_	V _{CC} × 0.3	_	V _{CC} × 0.3	V
				1.65	1.55	1.65	_	1.55	_	
			400 4	2.3	2.2	2.3	_	2.2	_	V
			$I_{OH} = -100 \mu A$	3.0	2.9	3.0	_	2.9	_	
		Maria Mar		4.5	4.4	4.5	_	4.4	_	
High-level output voltage	V_{OH}	VIN = VIL or VIH	I _{OH} = -4 mA	1.65	1.29	1.52	_	1.29	_	
Ü			$I_{OH} = -8 \text{ mA}$	2.3	1.9	2.15	_	1.9	_	
			I _{OH} = -16 mA	3.0	2.4	2.8	_	2.4	_	
			$I_{OH} = -24 \text{ mA}$	3.0	2.3	2.68		2.3		
			$I_{OH} = -32 \text{ mA}$	4.5	3.8	4.2	_	3.8	_	
	V _{OL}	VIN = VIH	I _{OL} = 100 μA	1.65	_	0	0.1	_	0.1	V
				2.3	_	0	0.1	_	0.1	
				3.0	_	0	0.1	_	0.1	
Low-level output voltage				4.5	_	0	0.1	_	0.1	
			I _{OL} = 4 mA	1.65	_	0.08	0.24	_	0.24	
			I _{OL} = 8 mA	2.3	_	0.1	0.3	_	0.3	
			I _{OL} = 16 mA	3.0	_	0.15	0.4	_	0.4	
			I _{OL} = 24 mA	3.0		0.22	0.55		0.55	
			I _{OL} = 32 mA	4.5		0.22	0.55		0.55	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5			±1		±10	μА
Quiescent supply current	Icc	V _{IN} = 5.5V	or GND	1.65 to 5.5	_	_	1	_	10	μА

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics Symbo	Cumbal	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
	Symbol	rest Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time	t _{pLH}	C_L = 15 pF, R_L = 1 M Ω	1.80 ± 0.15	2.0	5.3	9.6	2.0	9.8	- ns
			2.5 ± 0.2	0.8	3.2	5.3	0.8	5.7	
			3.3 ± 0.3	0.5	2.4	3.7	0.5	4.0	
			5.0 ± 0.5	0.5	1.9	2.9	0.5	3.2	
		$C_L = 50 \text{ pF},$ $R_L = 500 \Omega$	3.3 ± 0.3	1.5	3.0	4.6	1.5	4.9	
			5.0 ± 0.5	0.8	2.4	3.6	0.8	3.9	
Input capacitance	C _{IN}		0 to 5.5	_	4		_	_	pF
Power dissipation capacitance	Coo	(Note 3)	3.3	_	19		_	_	nE
	C _{PD} (Note 3)	5.5	_	27		_	_	pF	

Note 3: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

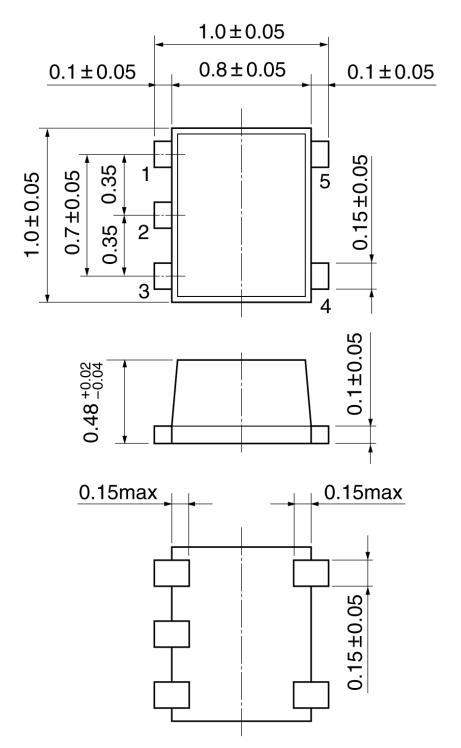
Average operating current can be obtained by the equation.

 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$



Package Dimensions

SON5-P-0.35 Unit: mm



Weight: 0.001 g (typ.)

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